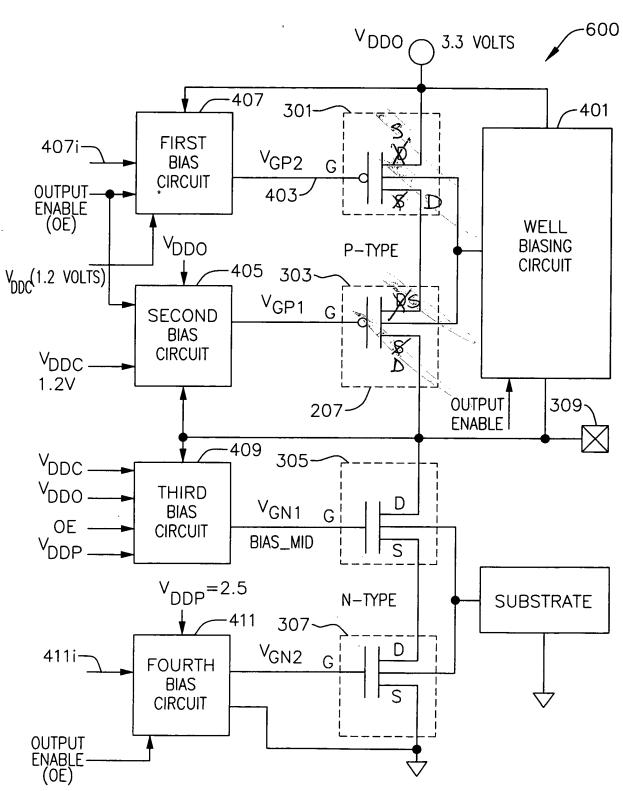


Application No. 10/621,005
Inventor(s): Janardhanan S. Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE
TOLERANT INPUT OUTPUT (I/O) CIRCUIT
WHICH ACCOMMODATES LARGE POWER
SUPPLY VARIATIONS
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FIG.6





Application No. 10/621,005 Inventor(s): Janardhanan S. Ajit Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT INPUT OUTPUT (I/O) CIRCUIT WHICH ACCOMMODATES LARGE POWER SUPPLY VARIATIONS Supp. Amdt. Dated December 2, 2004 Annotated Sheet 9 of 23

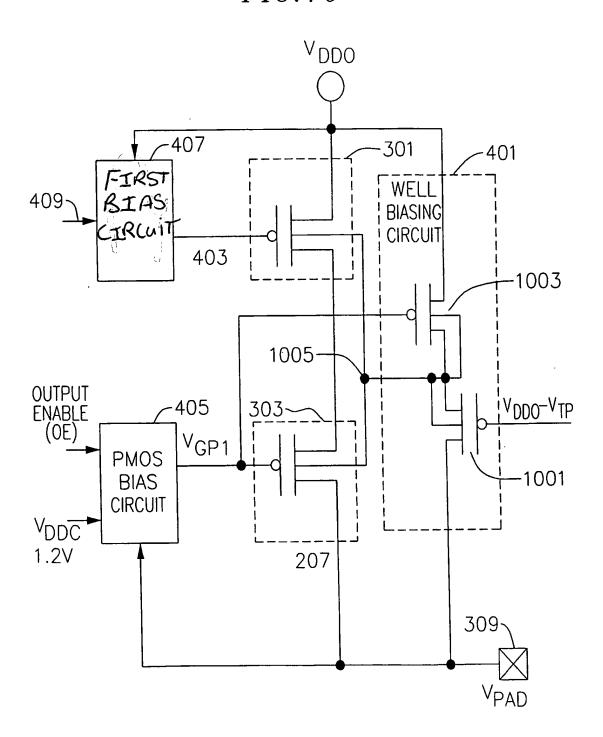
BIAS_MID Voop Voop 606 V_{DDP} 907 .903 905 BIAS_ Vpbo OE.

FIG.9A



Application No. 10/621,005 Inventor(s): Janardhanan S. Ajit Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT INPUT OUTPUT (I/O) CIRCUIT WHICH ACCOMMODATES LARGE POWER SUPPLY VARIATIONS Supp. Amdt. Dated December 2, 2004 Annotated Sheet 11 of 23

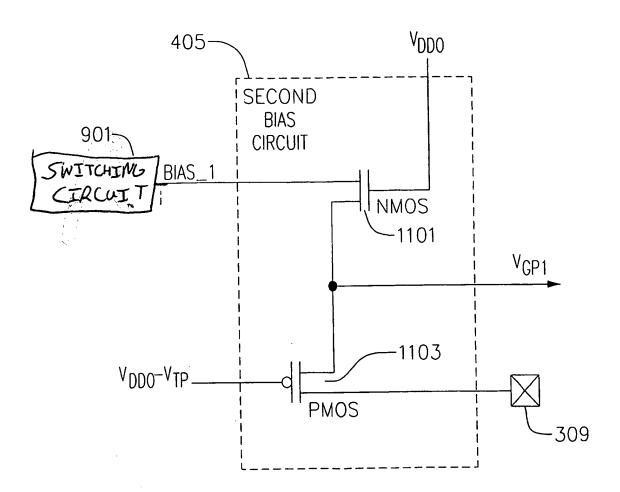
FIG. 10





Application No. 10/621,005 Inventor(s): Janardhanan S. Ajit Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT INPUT OUTPUT (I/O) CIRCUIT WHICH ACCOMMODATES LARGE POWER SUPPLY VARIATIONS Supp. Amdt. Dated December 2, 2004 Annotated Sheet 12 of 23

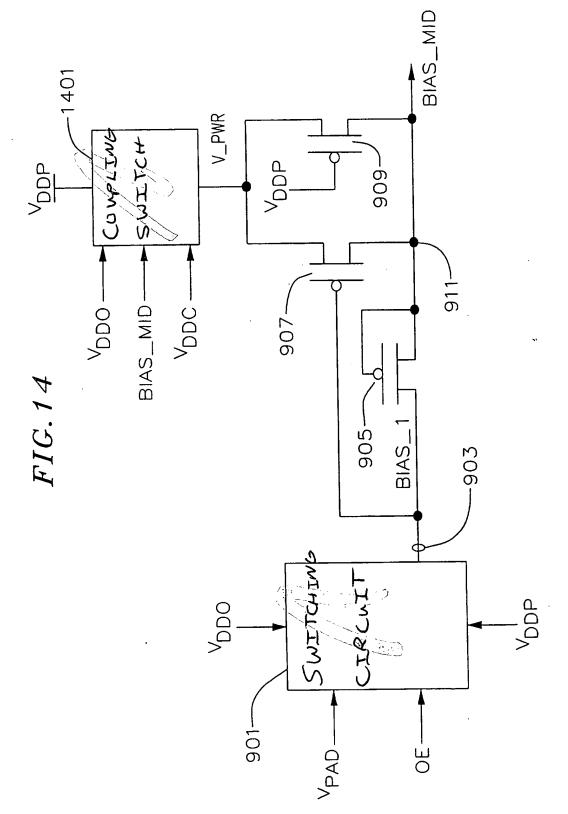
FIG. 11A



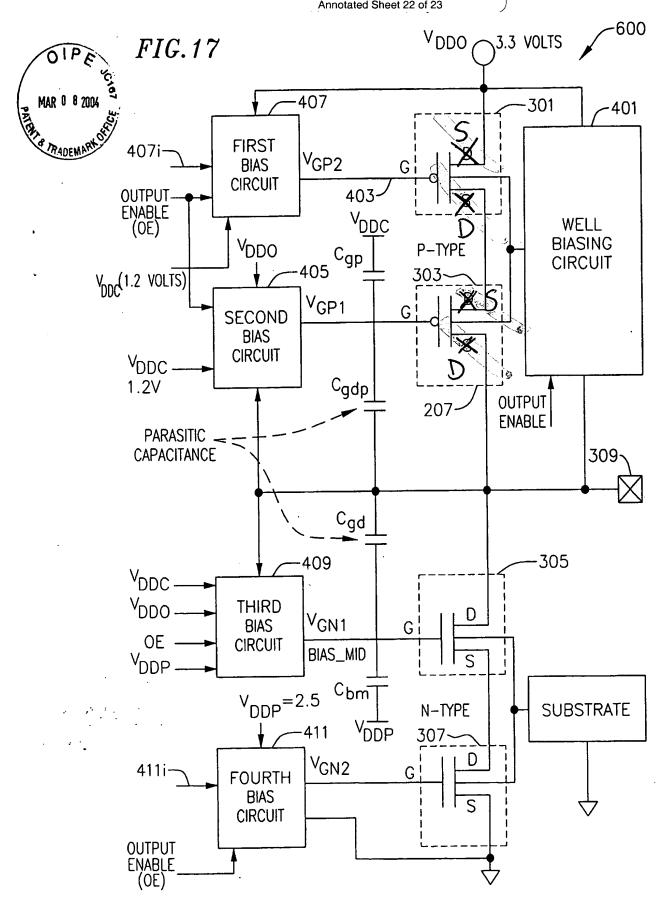
Application No. 10/621,005
Inventor(s): Janardhanan S. Ajit
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Annotated Sheet 17 of 23 V_{PAD} .1214 1220 1201 1203 1223 1225 1205 1209 1211 $\overline{\Lambda}^{DDO}$ 1207 1213 1219-1217 V_{DDO} BIAS_MID B VSSCOR VDDCOR VDDP -1255



Application No. 10/621,005 Inventor(s): Janardhanan S. Ajit Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT INPUT OUTPUT (I/O) CIRCUIT WHICH ACCOMMODATES LARGE POWER SUPPLY VARIATIONS Supp. Amdt. Dated December 2, 2004 Annotated Sheet 19 of 23



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FIG. 18

